VLSI DESIGN LAB – EL 521

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OBJECTIVE:

To give students practical hands-on experience with Electronic Design Tools. Students are expected to simulate digital and analog circuits. This will also enhance team work and project management skills of students in VLSI related projects.

PLAN:

1. Students will work in group of 2 or 3
2. Different projects in the area of Digital and Analog System will be allocated to the students

Project evaluation:

1. Presentation and Demonstration of project at high level (For digital blocks, Verilog level simulation and for analog blocks simulation using Cadence Spectre).
2. Presentation and Simulation for projects at Layout level.
3. Presentation and Demonstration of project on FPGA for digital systems

Course Evaluation

- Students will get evaluated on the projects that they do in the lab. Evaluation will include demonstration and documentation.